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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

| In re th | e Application of: | } |
|------------------|---|---|
| Sang-Deok Kim | | Art Group: |
| Application No.: | | Examiner: |
| Filed: | | |
| For: | Method for Fabricating Capacitor In Semiconductor Device | |
| P.O. E | nissioner for Patents Box 1450 ndria, VA 22313-1450 | |
| | TRANSMITTAL | OF FORMAL DRAWINGS |
| overpa | ngs, 8 sheets including 12 Figures. Pleas | pove-identified U.S. Patent Application are the formal se charge any additional fees or credit any 66. A duplicate copy of the Fee Transmittal is |
| | | Respectfully submitted, |
| Dated: | 7/16/03 | Blakely, Sokoloff, Taylor & Zafman LLP Eric S. Hyman, Reg. No. 30,139 |
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